

MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2019-20
II B.TECH I SEMESTER - SEC– A

Class In Charge: Dr. R. Sitharthan

With effect from: 08-07-2019

Room No: EB - 214

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00-5.00
MON	SPT Lab			B R E A K	EDC	NT	LSE	
TUE	EDC(B1)/DSD (B2)Lab				DSD	EVS		
WED	LSE	NT	EFAE		EDC	SEM		
THU	DSD	DSD	NT		LIB	EDC(B1)/DSD (B2)Lab		
FRI	EFAE	LSE	EDC		EFAE	DSD		
SAT	NT	GATE	EDC		Holistic Activities			

*Tutorial

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. K. S. Srinivsa Rao
LSE	18BIO101	Life Sciences for Engineers	Dr Balaji M
NT	18ECE101	Network Theory	Dr. R. Sitharthan
DSD	18ECE102	Digital System Design	Dr. K. R. Kashwan
EDC	18ECE103	Electronic Devices and Circuits	Dr. Thamizharasan. S
MC		Mandatory Course	Dr Balaji M
Seminar			Dr . Sourab Paul
Gate			Mr Rupesh Kumar
LABORATORY			
SPT Lab	18ENG201	Speaking through Listening Laboratory	Dr Suraj/Dr Banekar Tushar Vinayak
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Brijesh Kumar Singh / Mr. K. Rahim Bakash/V. Chandrasckhar
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Satrughan Kumar / Ms. J. Mary Angel Asha Latha/Manjula Mam

Time Table Coordinator

HOD

Principal

MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2019-20
II B.TECH I SEMESTER - SEC- B

Class In Charge: Dr. Sankata Bhanjan Prusty

With effect from: 08-07-2019

Room No: EB - 213

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10- 01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00-5.00
MON	EFAE	EDC	LSE	B R E A K	DSD (B1) / EDC (B2)			
TUE	NT	EFAE	EDC		EDC	EVS		
WED	SPT				DSD	DSD	LIB	
THU	DSD (B2) / EDC (B1)				NT	LSE	SEM	
FRI	DSD	DSD	NT		GATE	LSE		
SAT	NT	EDC	EFAE		Holistic Activities			

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. K. S. Srinivsa Rao
LSE	18BIO101	Life Sciences for Engineers	Dr. Ranjeeth B
NT	18ECE101	Network Theory	Dr. Anil Kumar Soni
DSD	18ECE102	Digital System Design	Dr. K. R. Kashwan
EDC	18ECE103	Electronic Devices and Circuits	Dr. Sankata Bhanjan Prusty
MC		Mandatory Course	Dr Balaji M
Seminar			Dr Debashish Dash
Gate			Mr.Chandramauleshwara Roy
LABORATORY			
SPT Lab	18ENG201	Speaking through Listening Laboratory	Dr Suraj/Dr Banekar Tushar Vinayak
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Gaurav Varshney (B1) / Dr. Sunandita Debnath (B2)/ Mr. R. Madhu Krishna/ V. Chandrasckhar
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Sankata Bhanjan Prusty / Ms. J. Mary Angel Asha Latha Manjula Mam

Time Table Coordinator

HOD

Principal

MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2019-20
II B.TECH I SEMESTER - SEC- C

Class In Charge: Mr. Jitendra Bahadur

With effect from: 08-07-2019

Room No: EB - 212

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10-01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00-5.00
MON	NT	EDC	EFAE	B R E A K	SEM	DSD	LSE	
TUE	EDC	LSE	DSD		GATE	EFAE		
WED	EFAE	EDC	NT		EDC (B1) / DSD (B2)			
THU	NT	LSE	EDC		SPT			
FRI	DSD(B1) / EDC (B2)				DSD	EVS		
SAT	DSD	NT	LIB		Holistic Activities			

*Tutorial

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. K. S. Srinivsa Rao
LSE	18BIO101	Life Sciences for Engineers	Dr. Arun babu D
NT	18ECE101	Network Theory	Dr. Brijesh Kumar Singh
DSD	18ECE102	Digital System Design	Dr. Parthapratim De
EDC	18ECE103	Electronic Devices and Circuits	Dr. Sumit Kale
MC		Mandatory Course	Dr. Arunbabu D.
Seminar			Dr. Shanidul Hoque
Gate			Dr. Sunandita Debnath
LABORATORY			
SPT Lab	18ENG201	Speaking through Listening Laboratory	Dr Suraj/Dr Banekar Tushar Vinayak
DSD Lab	18ECE201	Digital System Design Laboratory	Mr. Debashish Dash / Mr. R. Madhu Krishna/ V. Chandrasckhar
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Sumit Kale / J. Mary Angel Asha Latha (B2)/ Mr. K. Rahim Bakash (B1)/ Manjula Mam

Time Table Coordinator

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MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2019-20
II B.TECH I SEMESTER - SEC– D

Class In Charge: Dr. Ranjan Kumar Mahapatra

With effect from: 08-07-2019

Room No: EB - 211

DAY/ TIME	9.10 -10.10	10.10-11.10	11.10 -12.10	12.10–01.00	1.00 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00-5.00
MON	EDC(B1)/ DSD(B2)			B R E A K	SEM	LSE	EFAE	
TUE	SPT				EDC	EFAE		
WED	EDC	NT	LIB		NT	EDC	DSD	
THU	DSD	LSE	DSD		NT	EVS		
FRI	EFAE	LSE	GATE		LIB	EDC(B2)/ DSD(B1)		
SAT	DSD	NT	EDC		Holistic Activities			

*Tutorial

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. N. Shesadri
LSE	18BIO101	Life Sciences for Engineers	Dr. Balaj R
NT	18ECE101	Network Theory	Dr. Sambasiva Rao Kambala
DSD	18ECE102	Digital System Design	Dr. Lucky Agarwal
EDC	18ECE103	Electronic Devices and Circuits	Dr. Ranjan Kumar Mahapatra
MC		Mandatory Course	Dr. Chandramohan K.
Seminar			Dr. Brijesh Kumar Singh
GATE			Dr. Sourabh Paul
LABORATORY			
SPT Lab	18ENG201	Speaking through Listening Laboratory	Dr. S Shanmugapriya/ Dr. Kankan Das
DSD Lab	18ECE201	Digital System Design Laboratory	Dr. Arvind Kumar / Mr. R. Madhu Krishna (B2)/ Mr. K. Rahim Bakash (B1) / V. Chandrasekhar
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Thamizharasan. S / J. Mary Angel Asha Latha/ Manjula Mam

Time Table Coordinator

HOD

Principal

MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2019-20
II B.TECH I SEMESTER - SEC– E (Shift-2)

Class In Charge: Mr. Ravi Tiwari

With effect from: 08-07-2019

Room No: EB - 210

DAY/ TIME	1.00 – 2.00	2.00 – 3.00	03.00– 04.00	04.00–4.10	04.10– 5.10	5.10 – 6.10	6.10 – 7.10
MON	SPT			B R E A K	EFAE	NT	LSE
TUE	SEM	DSD	LIB		NT	DSD	EDC
WED	DSD	GATE	LIB		EDC (B2) /DSD (B1)		
THU	EDC (B1) / DSD (B2)				LSE	EDC	NT
FRI	DSD	EVS			LSE	EFAE	EDC
SAT	EDC	NT	EFAE		Holistic Activities		

*Tutorial

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. M. Venkata Ramana
LSE	18BIO101	Life Sciences for Engineers	Dr. P P George
NT	18ECE101	Network Theory	Dr. S. Rajasekaran
DSD	18ECE102	Digital System Design	Mr. Ravi Tiwari
EDC	18ECE103	Electronic Devices and Circuits	Dr. Remashan Kariyadan
MC		Mandatory Course	Dr. Arunbabu D.
Seminar			Dr. R. Sitharthan
Gate			Dr. Sunandita Debnath
LABORATORY			
SPT Lab	18ENG201	Speaking through Listening Laboratory	Dr. S. Shanmugapriya/ Dr. Lakshmi Devi
DSD Lab	18ECE201	Digital System Design Laboratory	Mr. Ravi Tiwari / Mr. R. Madhu Krishna (B1)/ Mr. K. Rahim Bakash (B2) / V. Chandrasckha (B1)r/ Mr. K. MD. Riyaz Ali (B2)
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Dr. Rameshan Kariyadan (B1)/ Mr. Biwajit Dwivedy (B2)/ J. Mary Angel Asha Latha (B2)/ Manjula Mam (B2)/ Mr. Y. Pradeep Kumar (B1)/ Mr. V. Mustafa (B1)

Time Table Coordinator

HOD

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MADANAPALLE INSTITUTE OF TECHNOLOGY AND SCIENCE, MADANAPALLE- 517 325
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2019-20
II B.TECH I SEMESTER - SEC– F (Shift-2)

Class In Charge: Dr. Dheeren Ku Mahapatra

With effect from: 08-07-2019

Room No: EB - 217

DAY/ TIME	1.00 – 2.00	2.00 – 3.00	03.00– 04.00	04.00–4.10	04.10– 5.10	5.10 – 6.10	6.10 – 7.10
MON	LIB	NT	LIB	B R E A K	DSD	LSE	EDC
TUE	SEM	LSE	DSD		EDC	EFAE	NT
WED	EDC	DSD	EFAE		DSD (B2)/ EDC (B1)		
THU	NT	EVS			EDC (B1)/ DSD (B2)		
FRI	SPT				NT	LSE	GATE
SAT	EFAE	DSD	EDC		Holistic Activities		

*Tutorial

THEORY			
	CODE	SUBJECT	NAME OF STAFF MEMBER
EFAE	18HUM101	Economics and Financial Accounting for Engineers	Dr. N. Shesadri
LSE	18BIO101	Life Sciences for Engineers	Dr. A. Selvaganapathy
NT	18ECE101	Network Theory	Dr. S. Rajasekaran
DSD	18ECE102	Digital System Design	Dr. Dheeren Ku Mahapatra
EDC	18ECE103	Electronic Devices and Circuits	Dr. Remashan Kariyadan
MC		Mandatory Course	Dr. Chandramohan K.
Seminar			Dr. Sambasiva Rao Kambala
Gate			Dr. Bibhuti Bhusan Pradhan
LABORATORY			
SPT Lab	18ENG201	Speaking through Listening Laboratory	Dr. Kankan Das / Dr Suraj
DSD Lab	18ECE201	Digital System Design Laboratory	Mr. Bibhuti Bhusan Pradhan / Mr. K. Rahim Bakash/ Mr. K. MD. Riyaz Ali
EDC Lab	18ECE202	Electronic Devices and Circuits Simulation Laboratory	Mr. Jitendra Bahadur / Dr. Mr. Biwajit Dwivedy

Time Table Coordinator

HOD

Principal